



# राष्ट्रीय प्रौद्योगिकी संस्थान दिल्ली

**NATIONAL INSTITUTE OF TECHNOLOGY DELHI**  
(An autonomous Institute under the aegis of Ministry of HRD, Govt. of India)

सेक्टर ए-७, इन्स्टिट्यूशनल एरिया, नरेला, दिल्ली-११००४०, भारत/Sector A-7, Institutional Area Narela, Delhi-110040, INDIA  
दूरभाष/Tele: +9111-27787500-503, फ़ैक्स/ Fax: +9111-27787503, वेबसाइट/Website: [www.nitdelhi.ac.in](http://www.nitdelhi.ac.in)

Adv. No.: NITD/EEE/RP-ARS/SERB/JRF/17/01

January 23, 2017

## Advertisement for Junior Research Fellow (JRF) Position

Applications are invited from highly motivated and eligible candidates for 1 (one) position of JRF in following SERB-DST funded sponsored research project:

**Project Title:** "Higher-Order DC-DC Converters for Power Management of DC Nano-Grid in Futuristic Smart Buildings"

**Funding Agency:** Science and Engineering Research Board (SERB) - DST, Government of India

**Name of the Project Investigator (PI):** Dr. Anmol Ratna Saxena

**Department:** Electrical and Electronics Engineering

**Duration of the project:** 3 Years (2016-2019)

### **Eligibility Criterion:**

#### ***Essential Qualification:***

- (i) M. Tech. or M.E. degree in Electrical Engineering / Electrical and Electronics Engineering with specialization in Power Electronics and Drives with first division.
- (ii) B. Tech. or B.E. degree in Electrical Engineering / Electrical and Electronics Engineering with first division.

#### ***Desirable Qualification & Experience:***

Candidates should have cleared GATE at any period of time.

It is highly desirable that the candidates have research experience in: modeling, design and control of power electronics converters (mainly dc-dc and dc-ac converters), integration of renewable energy sources with grid etc. and hands on experience in developing and working on hardware / laboratory prototype of the converters. Knowledge of simulation softwares like MATLAB (programming/Simulink), PSPICE and/or PSIM is also highly desirable.

#### ***Job Profile:***

The responsibility of the JRF will be to explore the opportunities and challenges of integrating renewable energy sources and energy storage devices for the formation of DC Nano-grid. The candidates will have to develop and work on hardware/laboratory prototype of the converters. Knowledge of microcontroller/dSP programming is desirable.

#### ***Duration of Appointment:***

Initially the appointment will be made for one year with can be extended upto 3 (three) years purely based on the performance. The position is co-terminus with the project.

#### ***Fellowship:***

A consolidated salary/fellowship of INR 25,000/- per month will be paid throughout the duration of project.



# राष्ट्रीय प्रौद्योगिकी संस्थान दिल्ली

**NATIONAL INSTITUTE OF TECHNOLOGY DELHI**  
(An autonomous Institute under the aegis of Ministry of HRD, Govt. of India)

सेक्टर ए-७, इन्स्टिट्यूशनल एरिया, नरेला, दिल्ली-११००४०, भारत/Sector A-7, Institutional Area Narela, Delhi-110040, INDIA  
दूरभाष/Tele: +9111-27787500-503, फ़ैक्स/ Fax: +9111-27787503, वेबसाइट/Website: [www.nitdelhi.ac.in](http://www.nitdelhi.ac.in)

## **Important Instructions:**

1. Candidate possessing the requisite qualification and experience should apply, in the attached format along with their updated CV latest by midnight of **12<sup>th</sup> February, 2017**.
2. Duly signed Scan copy of Application Form along with the scan copies of mark sheets/documents must be sent to Dr. Anmol Ratna Saxena (PI), only through email, at following address: ***anmolsaxena@nitdelhi.ac.in* with CC to *anmol.nitd@gmail.com***. Please write "Application for JRF in the Department of EEE" in the subject line.
3. The short-listed candidates, called for interview, will be intimated **only through emails**. Candidates are advised to check their email regularly. No TA/DA shall be paid to candidates for attending the Interview and/or joining the position.
4. The appointment is for time bound project and the candidate is required to work dedicatedly for the successful completion of the project. Selected candidate has to join immediately.
5. Selected candidate will be encouraged to enrol for Ph.D. as per rules of NIT Delhi.
6. Incomplete application forms and forms received after due date will be summarily rejected.
7. All the Terms and Conditions for this recruitment will be as per guidelines of Science and Engineering Research Board, Government of India.

Dr. Anmol Ratna Saxena  
Project Investigator & Assistant Prof.  
Department of Electrical & Electronics Engineering  
National Institute of Technology Delhi  
Sector-A7, Institutional Area  
Narela, Delhi-110040

Copy to:-

1. PS to Hon'ble Director for kind information please
2. Dean (R&C) I/c
3. Head of Department, Electrical & Electronics Engineering
4. Assistant Registrar
5. Scientific Officer (for displaying the advertisement on institute website please)
6. Secretary, SERB
7. Office Copy/Project File



# राष्ट्रीय प्रौद्योगिकी संस्थान दिल्ली

**NATIONAL INSTITUTE OF TECHNOLOGY DELHI**  
(An autonomous Institute under the aegis of Ministry of HRD, Govt. of India)

सेक्टर ए-७, इन्स्टिट्यूशनल एरिया, नरेला, दिल्ली-११००४०, भारत/Sector A-7, Institutional Area Narela, Delhi-110040, INDIA  
दूरभाष/Tele: +9111-27787500-503, फ़ैक्स/ Fax: +9111-27787503, वेबसाइट/Website: [www.nitdelhi.ac.in](http://www.nitdelhi.ac.in)

## Application Form

1.	<b>Name (in block letters):</b>		<b>Affix a Passport size Photograph</b>				
2.	<b>Father's Name:</b>						
3.	<b>Address for correspondence:</b> (Complete Address with pincode)						
				Mobile: _____			
				Email: _____			
4.	<b>Category (Gen./OBC/SC/ST):</b>						
5.	<b>Date of Birth: (dd/mm/yyyy)</b>						
6.	<b>Educational Qualification:</b>	(in following given order, starting from High School onwards)					
	<b>S.No.</b>	<b>Degree</b>	<b>Board/University</b>	<b>Year</b>	<b>Percentage Marks/CGPA</b>	<b>Division</b>	<b>Subjects</b>
7.	<b>Details of GATE Qualification</b> (if qualified)	<b>Qualification Year:</b>			<b>Validity:</b>		
		<b>Discipline:</b>			<b>Rank:</b>		
		<b>Score:</b>			<b>Marks obtained:</b>		
8.	<b>Experience</b> (Teaching/Industry/Research)	(Starting from latest)					
	<b>S. No.</b>	<b>Organization</b>	<b>Duration</b>	<b>Salary</b>	<b>Nature of Work</b>		
9.	<b>Title of M. Tech. Dissertation</b>						
10.	<b>Professional Skills</b> (Give details of hands on experience on simulation software and knowledge of hardware /experimental skills related to project)						

11.	<b>How the above position is helpful in meeting your career goal</b> (max. 250 words)		
12.	<b>Details of Research Publications</b> (if any)	Please attach separate sheet	
13.	<b>Any other relevant information:</b>		
14.	<b>References (Two)</b> (Name of two referees who are familiar with your academic and research background)		

I hereby declare that the above information is correct to the best of my knowledge and belief. I understand that my candidature will be cancelled if any information is found incorrect at any given period of time.

Signature with Name and Date