



NATIONAL INSTITUTE OF TECHNOLOGY DELHI
(An autonomous Institute under the aegis of Ministry of HRD, Govt. of India)

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September 03, 2019

WALK-IN INTERVIEW FOR VARIOUS POSITIONS UNDER SMDP – C2SD PROJECT (PURELY ON CONTRACT BASIS)

Walk-in Interview for various positions (purely on contract basis) are invited under Special Manpower Development Programme (SMDP) – Chip-to-System Design (C2SD) funded by Ministry of Electronics and IT, (MeitY) Govt. of India, in the department of Electronics and Communication Engineering of National Institute of Technology (NIT) Delhi.

S. no.	Designation	No. of posts	Job Requirement	Essential Qualification	Desirable	Consolidated Salary
1	Project Associate	One	Assist in execution of VLSI projects. Assist faculty members in planning and conducting of VLSI /Embedded related UG lab classes. Documentation work.	B. Tech in Electronics and Communication Engineering (ECE) with minimum CGPA of 6.5 and having working experience in VLSI related software.	Working knowledge in UNIX/MATLAB/scripting, Cadence, Mentor graphics, Synopsys TCAD etc.	Rs.20,000/- PM Consolidated.
2	Lab Engineer	One	Hardware and VLSI Software installation and maintenance, Linux Network Administration, VLSI /Embedded related PG lab conduction and related research	B.E. / B. Tech. in Electronics or Electronics & Communication or Computer Science or Electrical Engineering, with a minimum CGPA of 6.5 or minimum 60 % marks, having a working	M.E. / M. Tech. in VLSI Design or Microelectronics or equivalent. Working knowledge of VLSI CAD tools like Cadance/ Mentor/ Magma/ Tanner/ Xilinx &	Rs.34,000/- PM Consolidated.

			activities. Documentation work.	knowledge on Windows, Linux and VLSI Software tools.	Experience in Linux system administration.	
3.	Guest Faculty (Project)	One	Execution of VLSI projects, Teaching VLSI /Embedded related courses / lab and research activities. Documentation work.	Ph.D. in the field of VLSI Design or Microelectronics or equivalent	Ph.D. in field of VLSI & related Subjects, Publication in reputed journals, Working knowledge of VLSI tools like Cadance/ Mentor/ Synopsys/ Xilinx etc.	Rs. 40,000/-PM Consolidated.

The duration of above posts will be for six months or date of completion of project whichever is earlier. The candidates if selected for any of above post may be deputed anywhere in India for attending workshops/meeting related to the project.

Instructions

Candidates have to bring duly filled application form along with one copy of bio-data, all the original certificates, one set of self attested photocopy of all certificates and two latest passport size colour photographs on the scheduled date and time. Candidates are advised to check regularly the institute website for updates.

Date of Walk-in Interview: September 25, 2019

Reporting: 9:30 A.M. to 11:30 A.M. in Room No. 304. Interview will commence from 12 Noon onwards.

Contact Details:

Dr. Baljit Kaur

SMDP-C2SD Project

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Candidates should note the following points:

1. Selection committee of NIT Delhi reserves the right to fix suitable criteria for short- listing of eligible candidates.
2. No TA/DA will be provided to the candidates called for the interview.
3. Canvassing in any form will disqualify the candidate from the eligibility.
4. Provision for accommodation in NIT Delhi Hostel is not guaranteed.