## ABOUT THE INSTIUTE

National Institute of Technology Delhi (NITD) is one of the thirty-one NIT (s) established in the year 2010 by an Act of Parliament. The Institute has been declared as an Institute of National importance. NIT Delhi is ranked 45 in the NIRF ranking of MoE.

NIT Delhi is an autonomous Institute which functions under the aegis of Ministry of Education, Government of India. It aims to provide instructions and research facilities in various disciplines of Engineering, Science and Technology, Management, Social Sciences and Humanities for advance learning and dissemination of knowledge.

NIT Delhi has started its academic session in 2010 with three undergraduate B.Tech degree programs in Computer Science and Engineering, Electronics and Communication Engineering and Electrical and Electronics Engineering. Currently it offers B.Tech/M.Tech/Ph.D programs in various disciplines of Applied science, Civil Engineering, Computer science and engineering, Electrical Engineering, Electronics and Communication Engineering and mechanical and aerospace engineering.

## ABOUT THE DEPARTMENT

### VISION

Create an educational environment to prepare the students to meet the challenges of the modern electronics and communication industry through state of art technical knowledge and innovative approaches beneficial to society.

### MISSION

- To promote teaching and learning by engaging in innovative research and by offering state-of-the-art undergraduate, postgraduate, and doctoral programs.
- To cultivate an entrepreneurial environment and industry interaction, leading to the emergence of creators, innovators, and leaders.
- To promote co-curricular and extra-curricular activities for the overall personality development of the students.
- Building of responsible citizens through awareness and acceptance of ethical values

### GOALS

- Effective teaching and learning with continuous improvement in curriculum.
- Placements for all graduates and postgraduates
- Quality research publications
- Accreditations of programs offered
- MOU with industry and academia
- R&D projects

## ABOUT THE STC

All the Integrated Chips we use in mobiles, computers, nanodevices, and automobiles, etc. are designed with VLSI technology. Hence, there is a huge scope and growth in the VLSI Industry, and it is full of job opportunities. Since there is a huge gap between institute and the industry expectation, it is recommended to bridges that gap and gives you a great hands-on experience.

**Who can attend:** UG and PG students, PhD/Research scholars, Faculty from Universities, Technical and Professional Institutes. and industry personnel working in allied disciplines can also attend.

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**Short-Term Course
on
Analog ICs and Semiconductor
Advancements in VLSI
10 – 15 February 2025
(Hybrid Mode)**

**Organized By**



# National Institute of Technology Delhi
(An Institute of National Importance under Ministry of Education, Govt. of India)
**Department of Electronics and Communication Engineering**

**Chief Patron
Prof. (Dr.) Ajay K Sharma
Director**

**Patron
Dr. Rikmantra Basu
Head, Dept. of ECE**

| Conveners(s) | Coordinator(s) |
|---|---|
| Dr. D. Vaithiyanathan | Dr. Baljit Kaur |
| Dr. Manish Verma | Dr. Preeti Verma |

## Invited speakers/Resource Person:

1. **Prof. Guru Prasad Subas Chandra Mishra, NIT Raipur**

2. **Dr. Balwinder Raj, NIT Jalandhar**

3. **Dr. Varun Gopi, NIT Tiruchirappalli**

4. **Dr. Benchrif Hichem, Higher National Institute of Renewable energy, Algeria**

5. **Dr. Durga Devi, Collage of Engineering Guindy, Anna University**

6. **Dr. Wangkheirakpam Vandana Devi, NIT Manipur**

7. **Dr. Soumyaranjan Routray, SRM IST, Kattankulathur**

8. **Mr. Anish Kumar, Entuple Technologies**

## Topics to be Covered:

- Low Power VLSI Design
- Nano devices
- Advanced semiconductor materials in solar cell
- Analog IC Designs concepts and analysis
- Analog Custom Layout Design Concepts
- Analog and Digital Design Flow Using Cadence Tool
- Tutorials on Device simulations
- Case Study & Assignment

## Registration:

**Register for STTP through the following form link:** https://forms.gle/QhsnQ6zsmE8Kchmw8

**Last date for registration: 05th February 2025. Participants will be notified via email**

The registration fee for the course is as follows:

| | |
|---|---|
| UG | : ₹ 300/- |
| PG/PhD | : ₹ 400/- |
| Faculty | : ₹ 600/- |
| Industry personals | : ₹ 800/- |

Scan to Pay

OR

Deposit the fee in following account or scan to pay:

| | |
|---|---|
| Account Holder's Name | : Director National Institute of Technology Fee Collection |
| Account Number | : 092901001801 |
| IFSC Code | : ICIC0004610 |
| Bank Name & Branch | : ICICI Bank, Delhi-NIT Narela Subcity |

## Member(s):

- Prof.(Dr.) Jyoteesh Malhotra
- Prof.(Dr.) Manoj Kumar
- Dr. Manisha Bharti
- Dr. Sandeep Kumar
- Dr. Nitin Singh Singha
- Dr. Sachin Agrawal
- Dr. Dharmendra Kumar Jhariya
- Dr. Mahesh K. Singh

Institute Website: https://nitdelhi.ac.in/home/

Contact Us (for query): manishverma@nitdelhi.ac.in , dvaithiyanathan@nitdelhi.ac.in

**For any queries Please Contact: Dr. Manish Verma, Email: manishverma@nitdelhi.ac.in, Contact No.: +91-7694905199**