TOPICS TO BE COVERED

- Nanoelectronics & VLSI Design
- Low Power Reversible Architecture
- Quantum Technology in Semiconductors
- AI for VLSI Design
- Advances in MOS Transistor: From micro to nano
- Recent trends in VLSI Design
- Low Power High Speed Digital Integrated Circuits
- Hands-on Quantum tool
- Hands-on FPGA Flow
- Hands-on Digital Design Flow From RTL to GDS

INVITED SPEAKERS

Chief Guest
Shri Subodh Sachan, Director, STPI HQ

❖ Mr. Puneet Kumar, Senior Director R/D, Synopsys Inc

❖ Ms. Shelly Gupta, Founder & Director, AsicGuru Ventures

❖ Dr. Trailokya Nath Sasamal, Asst. Professor, NIT Kurukshetra

❖ Dr. Shashank Gavel, AI Lead, DataPOEM

❖ Dr. Soumya Ranjan Routray, SRM IST Kattankulathur

❖ Mr. Anish Kumar, Entuple Technologies Pvt. Ltd.

❖ Mr. Upendra Kumar Gupta, CoreEL Technologies (I) Pvt. Ltd.

NATIONAL INSTITUTE OF TECHNOLOGY DELHI

Department of Electronics & Communication Engineering

Organizing
Six Days Short Term Course
On

SILICON TO SYSTEMS: ADVANCING VLSI DESIGN IN AN AI AND QUANTUM-CONSCIOUS ERA

27 August- 2 September, 2025
(Hybrid Mode)

ABOUT NIT DELHI

An autonomous Institute that functions under the aegis of the Ministry of Education, Govt. of India. It aims to provide instructions and research facilities in various disciplines of Engineering, Science and Technology for advanced learning and dissemination of knowledge. Currently, the institute offers B.Tech. programs in Computer Science and Engg., Electronics and Comm. Engg., Electrical Engg., Civil Engg., Mechanical engg and VLSI Design and Technology. The institute also offers M.Tech., Ph.D., and Post-doctoral programs. NIT Delhi has been ranked 45 by NIRF 2024.

ABOUT THE STC

STC will explore the cutting-edge developments in VLSI design, from nanoscale devices to intelligent system architectures. Focusing on how AI and quantum technologies are shaping the future of chip design and automation. The course bridges theoretical foundations with hands-on exposure to modern design methodologies.

REGISTRATION

Register through the following link

https://forms.gle//B1DJibGoNNe3n6qy8

Last date of registration
20 August, 2025

The registration fee for the course:

UG: INR 400
PG/PhD: INR 600
Faculty: INR 800
Industry personnel: INR 1000
UG (NIT Delhi): INR 300
PG/PhD (NIT Delhi): INR 500

Deposit the fee in following account:
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Prof. (Dr.) Ajay K Sharma
Director, NIT Delhi

PATRON

Dr. Rikmantra Basu
HoD ECE, NIT Delhi

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Dr. Preeti Verma

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