



राष्ट्रीय प्रौद्योगिकी संस्थान दिल्ली

National Institute of Technology Delhi

(An autonomous Institute under the aegis of Ministry of Education, Govt of India)

SUMMER INTERNSHIP RESADV VLSI 2026

A Research-Focused Summer Internship Offering Practical
Training in Advanced VLSI Design Using Industry-Standard
EDA Tools and FPGA Platform

From Classroom Concepts to Chip-Level Design
Build. Simulate. Prototype

CONVENOR (S)

Dr. V. S. Pandey

Dr. D. Vaithyanathan

Dr. Preeti Verma

Dr. Manish Verma

WHO CAN JOIN

Undergraduate, Postgraduate Students
& Research Scholars



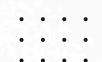
Last Date Of Registration
10 March 2026

List Of Selected Candidates
16 March 2026

Internship Duration
May- August 2026 (BatchWise)



Scan The QR
Code For More
Details And
Registration



For Queries

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